Appl. No. 09/460,965 Amdt. dated Dec. 14, 2004 Reply to Office Action of Oct. 12, 2004

## **Amendments to the Specification:**

Please replace the paragraph from page 2, line 26 through page 4, line 3, with the following rewritten paragraph:

Referring now to FIG. 1, a video system in accordance with the present invention will be discussed. Video system 110 100 is capable of reading information stored on an information storage medium 110, and receiving and processing the information as an input signal to processing system 112. In one embodiment, information storage medium 110 is capable of storing information in accordance with a known video standard and may be, for example, in the form of a video compact disc (VCD), digital video disc (DVD) or the like type of information storage medium. In one embodiment, the video information is encoded on information storage medium in compliance with a Moving Pictures Experts Group (MPEG) standard. In a typical VCD, for example, the video information may be encoded in accordance with an MPEG-1 standard, an MPEG standard designed for storing noninterlaced video and audio on a compact disc Processing system 112 reads information stored on information storage medium. information storage medium 110 as a system programmed bit stream and provides the bit stream as an output to a system to elementary converter 114. System to elementary converter 114 converts the MPEG-1 programmed bit stream received from processing system 112 to an MPEG-1 elementary bit stream, which is in turn provided to MPEG-2 decoder 115. MPEG-2 decoder decodes the MPEG-1 elementary bit stream into an MPEG-2 standard compliant signal where an MPEG-2 standard is defined as an extension of the MPEG-1 standard. An MPEG-2 standard compliant signal is optimized particularly for broadcast television including high definition television (HDTV). In contrast to MPEG-1, MPEG-2 provides interlaced video and provides a wider range of frame sizes. In one embodiment, for example, MPEG-2 decoder 116 provides a 352 by 240 pixel video frame size when information storage medium 110 is a typical VCD having MPEG-1 compliant video. In a particular embodiment, the video output signal of Appl. No. 09/460,965 Amdt. dated Dec. 14, 2004

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MPEG-2 decoder is encoded in a (YUV) type video format. The output of MPEG-2 decoder 116 is provided to an anti-block filter 124 which filters blocking present in the video output in accordance with the present invention. The output of anti-block filter 124 is then provided to DC display encoder 120 which converts the video frame into two fields each comprising a 240 by 720 pixel frame per field. The vide frames are then provided to interlacer 122 for providing an interlaced video signal output in a 720 by 480 YUV format to YUV to UYVY converter 124. YUV to UYVY converter 124 converts the video signal to a 720 by 480 pixel UYVY formatted video signal that is provided to display adapter 126. Display adapter 126 provides the video output signal to display 128 such that information stored on information storage medium 110 is displayed on display 128 as video. Although at least one or more embodiments of system 100 are discussed with respect to FIG. 1, one having skill in the art would recognize, upon reviewing the disclosure herein, that additional or alternative embodiments may be implemented, and at least one or more equivalent components thereof may be substituted, without providing substantial change to the function or structure of system 100 or to the scope of the present invention.

Please replace the paragraph from page 7, lines 3 - 15, with the following rewritten paragraph:

Referring now to FIG. 4, a sub-block partition in accordance with the present invention will be discussed. Sub-block 400 of FIG. 4 is substantially similar to sub-block 300 as shown in FIG. 3, however sub-block 400 is a predetermined partition of a video frame in both the horizontal and vertical directions. In one example, sub-block is an 8-pixel by 8-pixel partition of a video frame. As in FIG. 300 3, a first pixel 412 in a row of pixels 410 is disposed adjacent to a first vertical boundary 414, and a last pixel 416 in a row of pixels 410 is disposed adjacent to a second boundary 418. Likewise, a first pixel 422 in a column of pixels 420 is disposed adjacent to a first horizontal boundary 424, and a last pixel 426 in a column of pixels 420 is disposed adjacent to a second horizontal

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boundaries such as vertical boundaries 414 and 418 are filtered according to a horizontal filtering algorithm, and pixels adjacent to horizontal boundaries such as horizontal boundaries 424 and 428 are filtered according to a vertical filtering algorithm.